

1st ISSE'14 Workshop
1st workshop on the Integration of Safety and Security Engineering

Monday 08 September 2014
Florence, Italy

AGENDA

8:30	WELCOME - Morning Session	
	INTRODUCTION	
8:45	Introduction	L. Rioux & J. Favaro
9:00	ITEA MERGE Project "Current Works & Priorities"	C. Robinson (THALES)
9:30	ARTEMIS SESAMO Project "Work Achieved and Perspectives"	J. Favaro (INTECS)
10:30	BREAK	
	INVITED TALKS	
11:00	Integration of Security and Airworthiness in the Context of Certification and Standardization	J. Joyce (Critical Systems Labs)
11:45	Security considerations in automotive safety standardization activities	R. Mariani (Yogitech)
12:30	LUNCH	

14:00	Afternoon Session	
	TEHNICAL PAPERS	
14:00	From Safety Models to Security Models: Preliminary Lessons Learnt	P. Bieber, J. Brunel ONERA, France
14:30	FMVEA for Safety and Security Analysis of Intelligent and Co-operative Vehicles	C. Schmittner Zhendong Ma, and Paul Smith AIT, Austria
15:00	Uniform approach of risk communication in distributed IT environments combining safety and security aspects	Jana Fruth and Edgar Nett Uni Magdeburg, Germany
15:30	BREAK	
	PANEL & Discussions	
16:00	PANEL & Discussions "Safety and Security: Convergence or Concurrency?" Panelists: F. Vallée (All4Tec), S. Paul (THALES), M. Bouissou (EDF), J. Joyce (Critical Systems Labs), R. Mariani (YOGITECH)	Moderators: L. Rioux, J. Favaro
16:45	Conclusions & Closing Remarks	L. Rioux, J. Favaro
17:00	End of the workshop	

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INVITED TALKS

Invited Talk 1: “Integration of Security and Airworthiness in the Context of Certification and Standardization”

ABSTRACT: The increasingly integrated nature of electronic systems and network-intensive technologies in airborne systems, and the connection of these airborne systems with ground-based systems, demands the use of effective processes to ensure that the assessment of airworthiness hazards takes sufficient account of information security threats. This talk focuses on initiatives in the aerospace domain to strengthen the integration of safety and security engineering – in particular, the anticipated publication of RTCA DO 326A/EUROCAE ED 202A and its relationship to existing guidance for airworthiness such as SAE ARP 4754A (at the aircraft level) and RTCA DO 178C (at the software level). Beyond the aerospace domain, the general concepts and principles of these initiatives should be of interest to researchers and practitioners in other technical domains such as automotive, energy, rail and medical technology who similarly look towards greater integration of safety and security engineering.

BIO: Dr. Jeffrey Joyce is the co-founder of an engineering consultancy, Critical Systems Labs, that provides clients from across a variety of technical domains with expertise in the development of critical software-intensive systems. He has contributed to the development of both RTCA DO 178C (aerospace) and ISO 26262 (automotive). His recent technical achievements include co-invention of a method and system for ensuring operation of limited-ability autonomous driving vehicles, US Patent US8618922. Dr. Joyce earned a Ph.D. in Computer Science from Cambridge University in 1990 with earlier degrees from the University of Calgary and the University of Waterloo.

Invited Talk 2 (*provisional title*): “Security considerations in automotive safety standardization activities”

BIO: Dr. Riccardo Mariani holds a Ph.D. in Microelectronics from the University of Pisa. He is a member of the Italian and international committee of the ISO/TC22/SC3/WG16 (ISO26262) standard for Road Vehicles with special focus in the hardware area. Within the ISO26262 international working group, he is responsible for writing a guideline for microcontrollers and integrated circuits. Riccardo won the SGS-Thomson Award for best microelectronics degree and Enrico Denoth Best Engineering Award for best research in microelectronic at Pisa University. He was a VLSI designer and CAD laboratory responsible for the Centro TEAM (a VLSI design centre spin-off of Pisa University); consultant of Italtel-Siemens (office of Parma, Italy) for VLSI designs; VLSI designer, coordinator of digital circuit projects and eventually technical director of Aurelia Microelettronica, a company designing electronics for nuclear physics experiments and satellites. In August 2000 he co-founded Yogitech, acting at first as responsible for R&D and then as Chief Technical Officer, his current position.